# LOCO: Objects for Memory-Semantic Networks (WiP)

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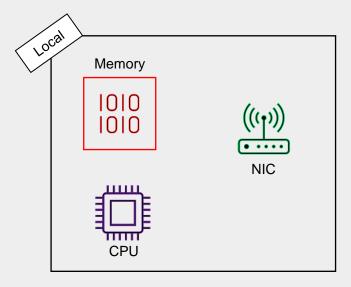
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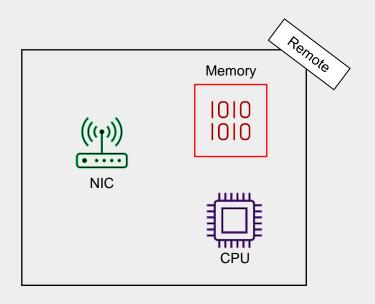
#### EMERALD 2024

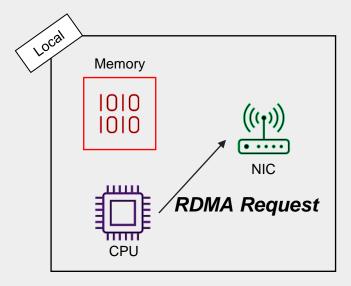


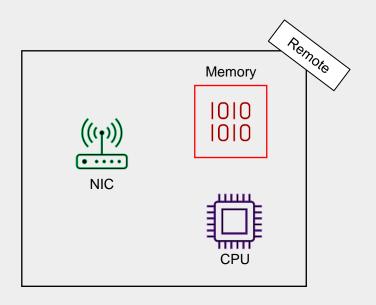


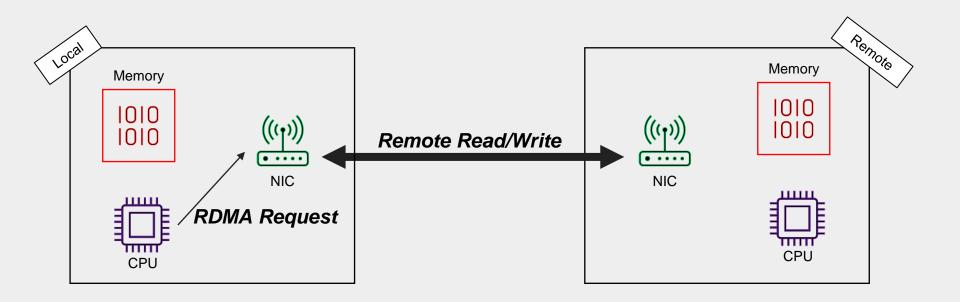
- 1. Motivation RDMA is fast, but hard to program
- 2. Related work Existing abstractions are either too complex (hard to use) or overly simple (limited performance)
- 3. Our contribution Objects are the solution

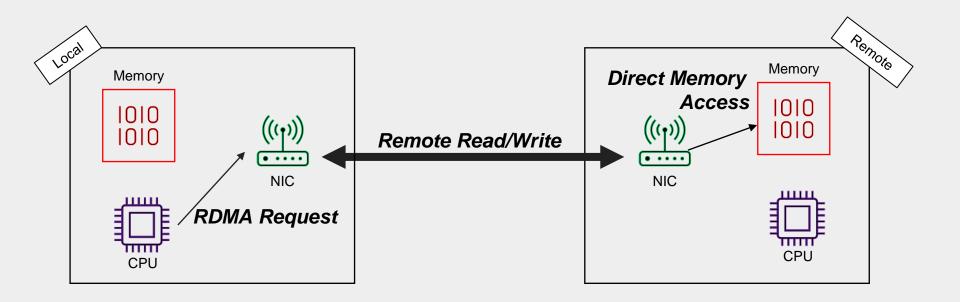


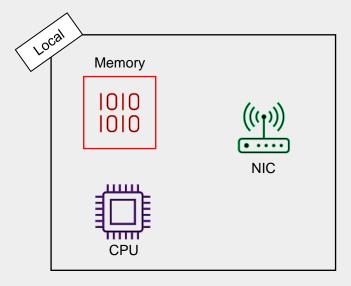


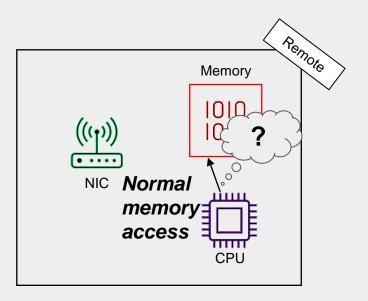












# How is RDMA Used?

#### **Directly through native verbs API:**

- Complex interface
- Must reason carefully about consistency
- Often used to (re-)implement a specific existing application
- Ad-hoc usage limits setup flexibility, failure handling

# How is RDMA Used?

### Through MPI:

- Used on the backend to accelerate existing interface
- Can also directly allocate memory regions for read/write access
- ..but scalability is limited due to coarse-grained, noncustomizable locking at the library level
- Other synchronization primitives (barriers) are library primitives as well

# How is RDMA Used?

#### As a single coherent address space:

- Enforcing coherence & consistency limits performance
- Naively porting shared memory applications gives poor performance due to extremely non-uniform latency

# Why Objects?

Objects are:

- Encapsulated They hide complexity from the user in a controllable way
- Composable and reusable Functionality can be reused and combined for new use cases
- Intuitive An object model is a good fit for many applications

# Existing object models

#### **BCL: Berkeley Container Library (ICPP '19)**

- Containers built on a flat global address space
- Implemented on top of MPI or similar using a client-server model
- Containers are unique, neither reusable nor composable

#### HCL: Hermes Container Library (CLUSTER '20)

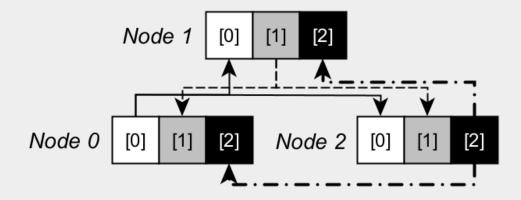
- Improved version of BCL
- Containers are now named and reusable, but still not composable
- Retains MPI backend & client-server model

# **LOCO Overview**

- Object system built directly on verbs API
- Memory is accessed through typed primitives
- Objects are composable and reusable, referred to by name
- Symmetric peer model which supports dynamic join & drop (at both object and node level)
- Different object implementations can connect to each other to support asymmetric behavior

### **Barrier example: class members**

- 1 class barrier : public loco::channel {
- 2 unsigned count, num\_nodes;
- 3 loco::sst\_var<unsigned> sst;



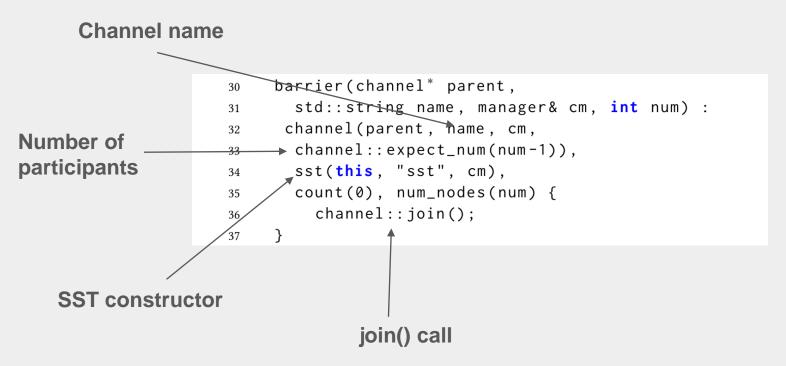
An SST with three participants. Arrows point from writers to readers.

# Barrier example: owned\_var

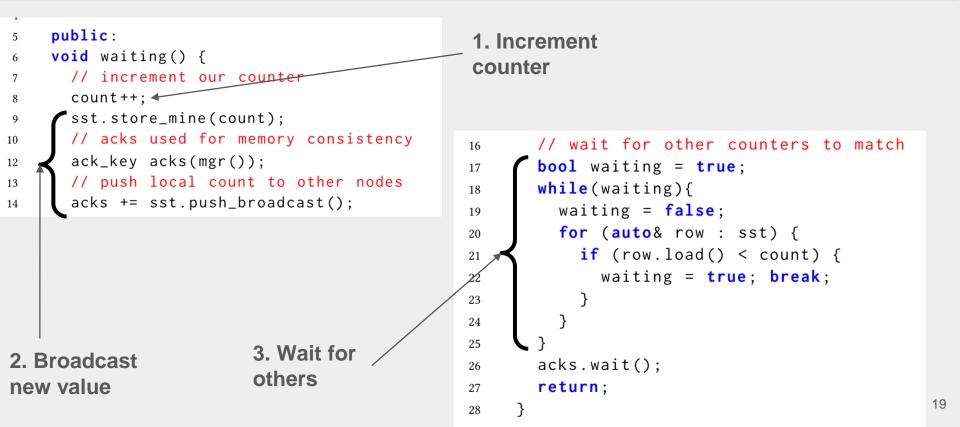
Each register in the SST is an owned\_var, which provides single-writer atomicity using different strategies depending on the size of the underlying type:

- ≤ 8 bytes: atomicity is guaranteed by the NIC-CPU interface
- ≤ 56 bytes: sequence numbers written before and after each data write; reader retries if they do not match
- > 56 bytes: attach checksum to writes, reader retries if they do not match

# **Barrier example**



# **Barrier example**



# **Barrier example**

```
1 class barrier : public loco::channel {
```

- 2 unsigned count, num\_nodes;
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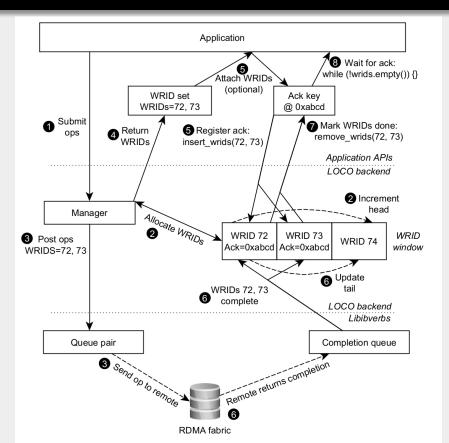
```
barrier(channel* parent,
30
      std::string name, manager& cm, int num) :
31
     channel(parent, name, cm,
32
     channel::expect_num(num-1)),
33
     sst(this, "sst", cm),
34
      count(0), num_nodes(num) {
35
        channel::join();
36
37
    }
```

5	public:
6	<pre>void waiting() {</pre>
7	<pre>// increment our counter</pre>
8	count++;
9	<pre>sst.store_mine(count);</pre>
10	<pre>// acks used for memory consistency</pre>
11	4 see Section 5.1
12	<pre>ack_key acks(mgr());</pre>
13	77 push local count to other nodes
14	acks += sst.push_broadcast();
15	
16	<pre>// wait for other counters to match</pre>
17	<pre>bool waiting = true;</pre>
18	<pre>while(waiting){</pre>
19	waiting = <pre>false;</pre>
20	<pre>for (auto&amp; row : sst) {</pre>
21	<pre>if (row.load() &lt; count) {</pre>
22	waiting = <b>true</b> ; <b>break</b> ;
23	}
24	}
25	}
26	acks.wait();
27	return;
28	}

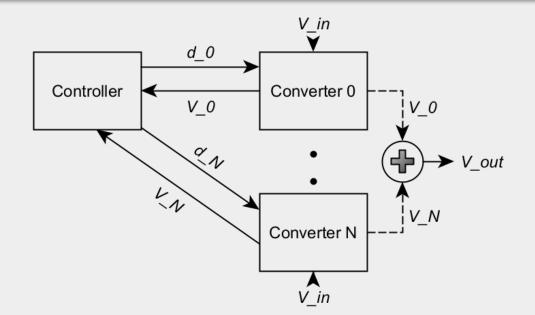


- An ack key is a pollable object representing used for monitoring the progress of one or more RDMA operations
- Each RDMA operation in LOCO corresponds to a 64-bit unsigned integer work request ID (WRID)
- A WRID can optionally be "attached" to an ack key to monitor progress of the corresponding operations
- The ack key is a bitset supporting lock-free insertion and removal
- The WRID is inserted in the bitset when attached, if not yet complete, and removed from the bitset when

## **Completion infrastructure**



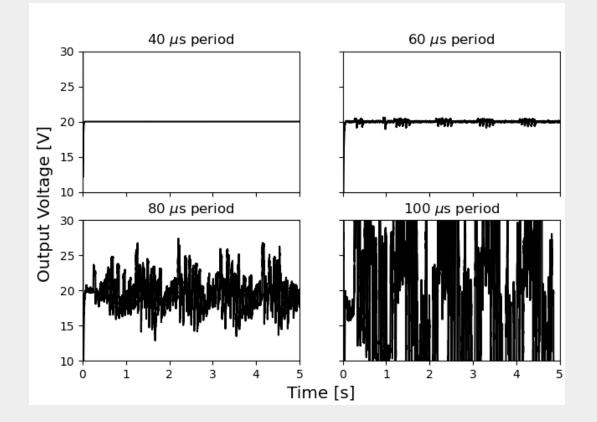
## **Distributed power converter**



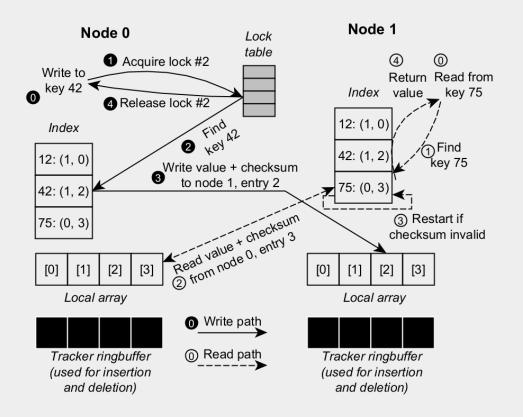
*V\_in:* individual input voltage *V\_out:* aggregate output voltage

*d\_N:* Duty cycle for converter N *V\_N:* Output voltage at converter N

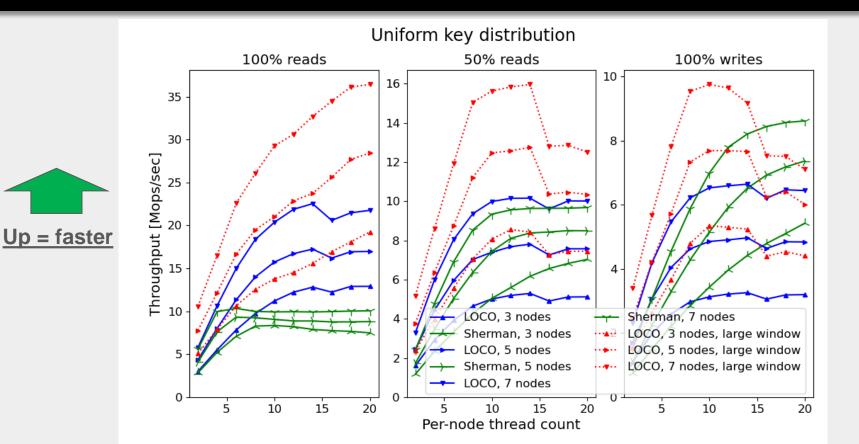
### **Power converter evaluation**



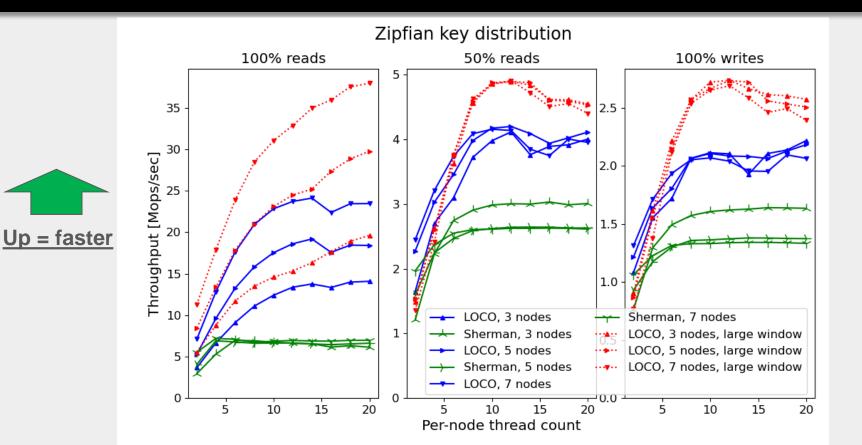
### **Distributed key-value store**



## **Key-value store evaluation**



# **Key-value store evaluation**





- RDMA is hard to program
- Existing abstractions limit performance or are difficult to use
- Objects present an attractive interface for hiding complexity while maintaining performance
- LOCO objects perform similarly to ad-hoc implementations

